

In the Abstract:

Please replace the abstract with the following rewritten Abstract:

A repeater system for receiving a modulated input signal and for transmitting a modulated output signal comprising a clock oscillator for providing a repeater system clock, a demodulator for demodulating an input signal ~~to obtain a demodulated signal~~ and a modulator for modulating the demodulated signal to obtain ~~the~~ a modulated signal. The demodulator comprises a first mixer, a first controllable oscillator and a feedback circuit, the first controllable oscillator being controlled by a first control value such that the frequency of the demodulated signal approaches a desired value. The modulator comprises a second mixer and a second controllable oscillator, the second controllable oscillator being controlled by a second control value such that the modulated output signal has a predetermined output frequency. A controller derives the second control value. ~~Since each of the output frequency influencing components of the repeater system is driven by the repeater system clock, and since the repeater system clock error is compensated for in the demodulator, the repeater system clock accuracy does not directly influence the accuracy of the output frequency. Thus, a moderately priced and moderately sized clock oscillator can be used for achieving an economical repeaters system.~~